

IN THE SPECIFICATION:

Please replace paragraph 111 in the specification with the following:

In an exemplary embodiment, these instructions take an address from a general register to fetch a large operand from memory, a second operand from a general register, perform a group of operations on partitions of bits in the operands, and catenate the results together, placing the result in a general register. An exemplary embodiment of the format 1210 of the wide translate instruction is shown in Fig. 13A.